# DIPLOMA EXAMINATION IN ENGINEERING/TECHNOLOGY/MANAGEMENT/ COMMERCIAL PRACTICE, APRIL - 2025

# EMBEDDED SYSTEM AND REAL TIME OPERATING SYSTEM

[Maximum marks: 75]

## PART A

### I. Answer all the following questions in one word or one sentence. Each question carries 1 mark. (9 x 1 = 9 Marks)

		$(9 \times 1 - 9 \text{ what ks})$	
		Module	Cognitive
		outcome	level
1	List any two application areas of Embedded system.	M1.01	U
2	AVR microcontroller follows architecture.	M1.02	R
3	The largest hex value that can be stored to an 8-bit register is	M2.02	R
4	Write the AVR C statement to set Port D as input port.	M2.03	R
5	Timer 1 of AVR is a bit timer.	M2.05	R
6	The RS pin is a pin for the LCD.	M3.01	R
7	In reading the column of a keyboard matrix, if no key is pressed, we	M3.01	R
	should get all values as		
8	Name the part of operating system which act as an abstraction layer	M4.01	R
	between system resource and user application.		
9	is also known as a lightweight process.	M4.03	R

### PART B

### II. Answer any eight questions from the following. Each question carries 3 marks.

		$(8 \times 3 = 24 \text{ Marks})$	
		Module	Cognitive
		outcome	level
1	Write any three purposes of Embedded system.	M1.01	U
2	State the role of DDRx, PORTx and PINx register in I/O operations.	M1.03	R
3	Write an AVR C program to get a byte of data from PORTB and	M2.02	А
	send it to PORTC continuously.		
4	Explain any three bit-wise logical operators in AVR C with	M2.03	U
	examples.		
5	Write any three sources of Interrupts in AVR microcontroller.	M2.07	U
6	Explain how to send data to LCD.	M3.01	U
7	Draw the block diagram of interfacing RS232 with ATMega32.	M3.01	U
8	Differentiate between Hard Real-time system and Soft Real-time	M4.02	U
	system.		

Reg.No..... Signature.....

[Time: 3 Hours]

9	Differentiate between Multiprocessing and Multitasking.	M4.04	U
10	Write any three Non-functional requirements in selecting a Real	M4.08	R
	time operating system.		

## PART C Answer all questions. Each question carries seven marks.

		(6 x 7 = 42 Marks)	
		Module	Cognitive
III	Describe the functions of each bit in the status register SREG.	outcome M1.03	level U
111		1011.05	U
	OR		
IV	Draw and explain the data memory organization of ATMega32.	M1.03	U
V	Write an AVR C program to get a byte of data from Port C. If it is	M2.02	А
	less than 100, send it to PORT B; otherwise to PORT D.		
	OR		
VI	Write an AVR C program to toggle all the bits of PORT B	M2.03	А
	continuously by		
	(i) Using the Inverting operator.		
	(ii) Using the EX-OR operator.		
VII	Write an AVR C program to convert packed BCD 0X42 to ASCII	M2.04	А
	and display the bytes on PORT C and PORT D.		
	OR		
VIII	Explain any two basic registers associated with AVR Timer.	M2.05	U
IX	Illustrate how a 4x4 matrix keyboard is interfaced with AVR.	M3.01	U
	OR		
Х	Explain ADC and write any two characteristics.	M3.02	U
XI	Explain any three functions of an Operating system.	M4.01	U
	OR		
XII	Explain process state transition diagram.	M4.03	U
XIII	Explain Round Robin scheduling algorithm.	M4.05	U
	OR		
XIV	Describe the role of device drivers in Realtime operating system.	M4.07	U

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