| TED (21) 4082   |
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| (Revision-2021) |

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# DIPLOMA EXAMINATION IN ENGINEERING/TECHNOLOGY/MANAGEMENT/ COMMERCIAL PRACTICE, APRIL - 2023

## MICROCONTROLLER PROGRAMMING AND APPLICATIONS

[Maximum marks: 75] (Time: 3 Hours)

#### PART A

I. Answer all the following questions in one word or one sentence. Each question carries 1 mark

 $(9 \times 1 = 9 \text{ Marks})$ Module Cognitive level outcome Write the function of RST pin of 8051 M1.01U What is the size of internal RAM of 8051? M1.03R List the external hardware interrupts of 8051. M2.02R What is the use of stack in 8051? M2.03U Define addressing mode. M3.01U 5 Ū The instruction ADD A, R7 is an example of which addressing mode. M3.01Write an example of a data transfer instruction. M3.02U Name an ADC chip for interfacing with 8051. M4.03R 8 How many data lines are there in a 16 x 2 alphanumeric LCD? R M4.01

#### PART B

### II. Answer any eight questions from the following. Each question carries 3 marks.

 $\begin{array}{|c|c|c|c|c|} \hline (8 \times 3 = 24 \text{ Marks}) \\ \hline \text{Module} & \text{Cognitive} \\ \hline \end{array}$ 

|    |   | Module outcome | Cognitive level |
|----|---|----------------|-----------------|
| 1  | Draw the PSW of 8051 and list its bits.   | M1.04          | U               |
| 2  | Write the dual functions of pins: ALE/ $\overline{PROG}$ and $\overline{EA}$ /Vpp | M1.01          | U               |
| 3  | Draw the TCON register and list its bits.   | M1.01          | U               |
| 4  | Describe the idle mode in 8051.   | M2.05          | U               |
| 5  | Define interrupt and list the interrupts from higher to lower priority in 8051.   | M2.02          | U               |
| 6  | With examples, describe immediate and direct addressing modes of 8051.            | M3.01          | U               |
| 7  | With examples, describe the following instructions: (i) CPLA (ii) SWAPA           | M3.02          | U               |
|    | (iii) CLRA  |                |                 |
| 8  | Write 8051 instructions to copy the value 55H to accumulator and copy the         | M3.02          | A               |
|    | data in accumulator to the location pointed by R0.                                |                |                 |
| 9  | Write any three LCD commands with its corresponding Hex Code                      | M4.01          | U               |
| 10 | Draw the interfacing diagram of ADC with 8051.                                    | M4.03          | U               |

PART C
Answer all questions. Each question carries seven marks

 $(6 \times 7 = 42 \text{ Marks})$ 

|      |  | $(0 \times 7 - 42 \text{ Marks})$ |                   |
|------|--|-----------------------------------|-------------------|
|      |  | Module                            | Cognitive         |
|      |  | outcome                           | level             |
|      |  |                                   |                   |
| III  | List all the pins of 8051.   | M1.01                             | U                 |
|      | OR   |                                   |                   |
|      |  |                                   |                   |
| IV   | Describe the internal RAM organization of 8051                     | M1.03                             | U                 |
|      |  |                                   |                   |
|      |  |                                   |                   |
| V    | With a diagram, describe the block diagram of microcontroller 8051 | M1.02                             | U                 |
| '    | a diagram, describe the clock diagram of intercontroller 0051      | 1,11,02                           | Ũ                 |
|      | OR   |                                   |                   |
| VI   | Describe about Stack pointer and Program Counter of 8051.          | M1.04                             | U                 |
| V 1  | Describe about Stack pointer and Frogram Counter of 8031.          | 1011.04                           | U                 |
| 3711 | Durany the TMOD resistant and describe the mode 0 and mode 1       | M2 01'                            | TT                |
| VII  | Draw the TMOD register and describe the mode 0 and mode 1          | M2.01`                            | U                 |
|      | operation of timer.  |                                   |                   |
|      | OR   |                                   |                   |
|      |  |                                   |                   |
| VIII | Write the steps in programming 8051 to receive data serially.      | M2.04                             | U                 |
|      |  |                                   | U                 |
| IX   | Write an ALP to find square of a number present in 9000H and store | M3.03                             | A                 |
|      | the square in 9001H and 9002H.                                     |                                   |                   |
|      | OR   |                                   |                   |
| X    | Write an ALP to divide two numbers present in 4000H and 4001H and  | M3.03                             | A                 |
| **   | store the quotient and remainder in 4002H and 4003H respectively.  | 1715.05                           | <i>1</i> <b>1</b> |
| XI   |  | M3.02                             | U                 |
| Al   | With examples, explain the rotate instructions of 8051.            | 1015.02                           | U                 |
|      | OR   |                                   |                   |
| 3777 | W'A ANT ODE ANT COLUMN   | ) /2 02                           | **                |
| XII  | With examples, explain ANL, ORL and XRL logic instructions of      | M3.02                             | U                 |
|      | 8051.  |                                   |                   |
| XIII | With necessary diagram, explain the interfacing of LM35 with 8051  | M4.04                             | U                 |
|      | OR   |                                   |                   |
|      |  |                                   |                   |
| XIV  | With necessary diagram, explain the interfacing of DC Motor with   | M4.07                             | U                 |
|      | 8051.  |                                   |                   |
|      | 1 0001.  |                                   |                   |

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